

ABSTRACT OF THE DISCLOSURE

A level shift circuit including a capacitor, a charge control circuit, and a limiting circuit. The charge control circuit is connected to the capacitor to provide the voltage of a high potential power supply to the capacitor and to control the charging of the capacitor. The limiting circuit is connected to the high potential power supply and the charge control circuit to limit the voltage provided to the capacitor from the high potential power supply before the charge control circuit stops providing the voltage of the high potential power supply to the capacitor.

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